# OPTIMUM THREE MODULI SET FOR ANY WORD BIT LENGTH N AND I $=0,1,2$ 

Gurinder Bawa<br>Stellantis, Auburn Hills, MI, USA


#### Abstract

In Residue Number System (RNS) the most important factor is the choice of optimum moduli set. The most used three moduli set is $M_{0}=\left(2^{n}-1,2^{n}, 2^{n}+1\right)$, but it is only optimum when the word bit length $N$ is exactly divisible by 3. A new three moduli set is proposed ( $2^{n}-1$, $2^{\mathrm{n+i}}, 2^{\mathrm{n}}+1$ ), where $\mathrm{i}=|\mathrm{N}|_{3}$. It is shown to have the same dynamic range efficiency as $M_{0}$ for any $N$. This new Moduli set is implemented using MRC II and CRT. The CRT implementation is shown to have same architecture for any $i$. The hardware consists of 6 n FA, $2 n+1+i$ inverters with propagation delay of $2 n+2$.


Keywords-Residue to Binary Converters, Residue Number System, Chinese Remainder Theorem II.

## I. INTRODUCTION

RNS has attracted many researchers for the past few decades that involve extensive computation in the field of signal processing such as fast Fourier Transform (FFT), Image Processing, and Digital Filters [1],[2]. High speed and less complexity of hardware requires the large number to be split into smaller numbers so that instead of one large computation several small computations can be executed in parallel which reduces the load and time of calculation. Thus, choice of proper moduli set could greatly make the design efficient and cost effective. RNS moduli sets which are discovered till now are mostly in the form of power of 2's. Further the moduli set could be a group of 3,4 or 5 depending upon the complexity and the dynamic range of system.
In this paper we proposed a new three moduli set which overcomes the limitation of conventional and mostly commonly used three moduli set $\left(2^{n}-1,2^{n}, 2^{n}+1\right)$ [3][4]. In our work we considered three cases:

Let N be the Word Bit Length.

1. If $|\mathrm{N}|_{3}=0$ then use moduli set $\mathrm{M}_{0}=\left(2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1\right)$ which is same as the most popular moduli set.
2. If $|\mathrm{N}|_{3}=1$ then usemoduli set $\mathrm{M}_{1}=\left(2^{\mathrm{n}}-1,2^{\mathrm{n}+1}, 2^{\mathrm{n}}+1\right)$.
3. If $|\mathrm{N}|_{3}=2$ then use moduli $\operatorname{set}_{2}=\left(2^{\mathrm{n}}-1,2^{\mathrm{n}+2}, 2^{\mathrm{n}}+1\right)$.

Once the correct moduli set is chosen, the next target it to convert RNS into binary system so that RNS based computations could be interfaced with the real-world binary computers. In our work we implemented the ideas of M.Akkal and P. Siy [5] using MRC-II and Ahmad Hiasat and Andraos

Sweidan [6] using CRT. In the later sections we have also concluded that our new moduli set is better in terms of efficiency.

## II. BACKGROUND

There are different three moduli set that are proposed in the literatures, but the most popular three moduli set is $\left(2^{n}-1,2^{n}\right.$, $2^{\mathrm{n}}+1$ ) [3]. Some other three moduli sets are $(2 \mathrm{n}-1,2 \mathrm{n}$, $2 \mathrm{n}+1)[7],(2 \mathrm{n}, 2 \mathrm{n}+1,2 \mathrm{n}+2)[8],\left(2^{\mathrm{n}}-1,2^{\mathrm{n}}+1,2^{2 \mathrm{n}}+1\right)[11],\left(2^{\mathrm{k}-1}-1\right.$, $\left.2^{\mathrm{k}}-1,2^{\mathrm{k}}\right)[9],\left(2^{\mathrm{n}}-1,2^{\mathrm{n}}+1,2^{2 \mathrm{n}+1}\right)[10],\left(2^{\mathrm{n}}, 2^{\mathrm{n}}-1,2^{2 \mathrm{n}-1}-1\right)$ [12], $\left(2^{\mathrm{n}-1}, 2^{\mathrm{n}}-1,2^{\mathrm{n}}+1\right)[6]$ have been proposed. Even all of the above-mentioned moduli sets have been proposed and claims some advantages, but no study yet has been carried out to compare these moduli sets.
A. Characteristics of Moduli Set: Dynamic Range and Dynamic Range Efficiency can be used to compare moduli set.

1) The Dynamic Range is defined as the product of moduli in a given set.
$\mathrm{M}=\prod_{i=1}^{3} m_{i}$
2) Dynamic range efficiency ' $\mathbf{D}_{\text {eff }}$ ' is the ratio of dynamic range of the moduli set to dynamic range of word bit length.
$D_{\text {eff }}=M / 2^{N}$
(2)

The following example will illustrate how those abovementioned parameters are calculated. Let us consider word bit length $\mathrm{N}=9$ bit. According to $\mathrm{M}_{0}$ the three moduli set is $2^{\mathrm{n}}-1$, $2^{n}, 2^{n}+1$.
where n is calculated as $\mathrm{n}=\mathrm{N} / 3=9 / 3=3$
If we substitute the above calculated value of $n$ as 3 then the three moduli sets are:
$\mathrm{m}_{1}=2^{3}-1=7 \mathrm{~m}_{2}=2^{3}=8 \quad \mathrm{~m}_{3}=2^{3}+1=9$
a. Dynamic rangeaccording to (1) is given as:
$=\left(2^{\mathrm{n}}\right)^{*}\left(2^{\mathrm{n}}-1\right)^{*}\left(2^{\mathrm{n}}+1\right)=2^{3 \mathrm{n}}-2^{\mathrm{n}}=504$.
b. Dynamic Range Efficiency ( $\mathbf{D}_{\text {eff }}$ ) according to (2) is:

$$
=\left(2^{3 n}-2^{n}\right) / 2^{3 n}=1-1 / 2^{2 n}
$$

$=1-1 / 2^{2 * 3}=1-1 / 64=.99$ or $99 \%(5)$
The efficiency of $\mathrm{M}_{0}$ is the reason why $\mathrm{M}_{0}$ is the most cited 3 moduli set. It will be shown that efficiency disappears if $|\mathrm{N}|_{3} \neq$ 0 .

1. Case1: $|\mathrm{N}|_{3}=1$

ForN $=10$, when divided by 3 gives us remainder 1 and $\mathrm{n}=(\mathrm{N}-1) / 3$
Value of n when calculated comes out to be 3 and the modulus set is $7,8,9$.
Dynamic range according to (1) is 504 .
Dynamic range efficiency ( $\mathrm{D}_{\text {eff }}$ ), when calculated according to (2)

$$
\begin{equation*}
\mathrm{D}_{\mathrm{eff}}=504 / 1024 \cong 50 \% \tag{6}
\end{equation*}
$$

This 3 Moduli set can only represents $50 \%$ of the desired dynamic range.
2. Case2: $|N|_{3}=2$

ForN $=11$, which when divided by 3 gives remainder 2 . Sothe value of $n$ is calculated using and $n=(N-2) / 3$
Calculating the value of $n$ yields 3 and the modulus set is 7,8 , 9.

Dynamic range according to (1) comes out to be 504.
Dynamic range efficiency ( $\mathrm{D}_{\text {eff }}$ ), when calculated according to (2):
$D_{\text {eff }}=504 / 2048 \cong 25 \%$
Again this 3 moduli set can only represents $25 \%$ of thedesired dynamic range.

## III. CORRECTED MODULI SET

According to the most popular moduli set the three moduli set is $\mathrm{M}_{0}=2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1$. But as we discussed in above section that it is no longer efficient if N is not exactly divisible by 3 . In order to overcome these shortcomings, we had approached the problem in different way and thus come up with a unique approach of solving this problem. We took two cases again.

1. When bit word length ' $N$ ' divided by 3 gives remainder 1
2. When bit word length ' $N$ ' divided by 3 gives remainder 2 . The modified moduli set for:
3. When $|N|_{3}=1$

In this case we had increased the central modulus by one so that our new moduli set looks like $\mathrm{M}_{1}=2^{\mathrm{n}}-1,2^{\mathrm{n}+1}, 2^{\mathrm{n}}+1$. We had further verified that this is the most efficient moduli set.

Example 3.1: Let us consider $\mathrm{N}=10$. The moduli set according to our modified formula is; $\mathrm{M}_{1}=2^{\mathrm{n}}-1,2^{\mathrm{n}+1}, 2^{\mathrm{n}}+1$ is 7, 16, 9.
(a) Dynamic range: As defined in the section 2.1, the dynamic range is the product of all the modulus in the moduli set and is given as:

$$
\begin{align*}
& \mathrm{M}=\prod_{i=1}^{3} m_{i}=\left(2^{\mathrm{n}}-1\right)^{*}\left(2^{\mathrm{n}+1}\right)^{*}\left(2^{\mathrm{n}}+1\right) \\
& =\left(2^{\mathrm{n}+1}\right)^{*}\left(2^{2 \mathrm{n}}-1\right)=2^{3 \mathrm{n}+1}-2^{\mathrm{n}+1} \tag{8}
\end{align*}
$$

$\mathrm{N}=3 \mathrm{n}+1$ :
$\mathrm{n}=(10-1) / 3=3$
Substituting the value of n in (8)

$$
\begin{equation*}
M=\left(2^{3 * 3+1}-2^{3+1}\right)=(1024-16)=1008 \tag{9}
\end{equation*}
$$

(b) Dynamic Range Efficiency $\mathbf{D}_{\text {eff }}$ is defined as the ratio of dynamic range $(\mathrm{M})$ to dynamic range of bit word length.
$D_{\text {eff }}=M / 2^{\mathrm{N}}=\frac{\left(2^{3 n+1}-2^{n+1}\right)}{2^{3 n+1}}$
$=1-\frac{1}{2^{2 n}}=1-\frac{1}{64}=.99$ or $99 \%$
This is same as equation (5).

1. When $|N|_{3}=2$

In this case we had increased the central modulus by two so that our new moduli set $\mathrm{M}_{2}=2^{\mathrm{n}}-1,2^{\mathrm{n}+2}, 2^{\mathrm{n}}+1$. In order to verify that this is most efficient moduli set we took an example as shown below.
Example 3.2: Let us consider $\mathrm{N}=11$. The moduli set according to the modified formula is; $\mathrm{M}_{2}=2^{\mathrm{n}}-1,2^{\mathrm{n}+2}, 2^{\mathrm{n}}+1$ is 7, 32, 9 .
(a) Dynamic range: The dynamic range is the product of all the modulus in the moduli set and is given as:
$\mathrm{M}=\prod_{i=1}^{N} m_{i}=\left(2^{\mathrm{n}+2}\right) *\left(2^{2 \mathrm{n}}-1\right)=2^{3 \mathrm{n}+2}-2^{\mathrm{n}+2}$
$\mathrm{N}=3 \mathrm{n}+2$;
$\mathrm{n}=(11-2) / 3=3$
And if we substitute the above value of n in (11) we get
$\mathrm{M}=\left(2^{3^{* 3+2}}-2^{3+2}\right)=(2048-32)=2016$
(b) Dynamic Range Efficiency $\mathbf{D}_{\text {eff }}$ is defined as the ratio of dynamic range (M) to dynamic range of bit word length.
$D_{\text {eff }}=M / 2^{\mathrm{N}}=\frac{\left(2^{3 n+2}-2^{n+2}\right)}{2^{3 n+2}}=1 \frac{1}{2^{2 n}}=1 \frac{1}{64}=.99$ or $99 \%$
This is same as equation (5).
The results can be summarized in the following theorem.

# International Journal of Engineering Applied Sciences and Technology, 2024 <br> Vol. 8, Issue 11, ISSN No. 2455-2143, Pages 161-173 <br> Published Online March 2024 in IJEAST (http://www.ijeast.com) 

Theorem 1: For anyword bit length N, the optimized 3 moduli set are given below:

1. If $|N|_{3}=0$ so $\mathrm{N}=3 \mathrm{n}$, then Modlui set is given as $\mathrm{M}_{0}=$ $\left(2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1\right)$.
2. If $|N|_{3}=1$ so $\mathrm{N}=3 \mathrm{n}+1$ then Modlui set is given as $\mathrm{M}_{1}=$ $\left(2^{\mathrm{n}}-1,2^{\mathrm{n}+1}, 2^{\mathrm{n}}+1\right)$.
3. If $|N|_{3}=2$ so $\mathrm{N}=3 \mathrm{n}+2$ then Modlui set is given as $\mathrm{M}_{2}=$ ( $2^{\mathrm{n}}-1,2^{\mathrm{n}+2}, 2^{\mathrm{n}}+1$ ).

With $D_{\text {eff }}=1-\frac{1}{2^{2 n}}$

## IV. RNS TO BINARY CONVERTER

Once we have the correct moduli set, the next target undoubtedly should be to ascertain the appropriate RNS to Binary converter and then plugging the RNS arithmetic to

Binary world. For that we have followed the work of M.Akkal and P. Siy [5] using MRC-II and Ahmad Hiasat and Andraos Sweidan [6] using CRT.

## A. RNS to Binary Converter using MRC II

According to MRC II [5] number X is represented as: $\mathrm{X}_{\mathrm{i}}=\mathrm{a}_{\mathrm{i}} \mathrm{M}_{\mathrm{i}-1}+\mathrm{X}_{\mathrm{i}-1}$ for $2 \leq \mathrm{i} \leq \mathrm{n} ; \mathrm{n} \geq 2$

Where $a_{i}$ is the MRC coefficient, $n$ is the number of moduli, $\mathrm{X}_{1}=\mathrm{a}_{1}=\mathrm{r}_{1}, \mathrm{X}_{\mathrm{n}}=\mathrm{X}$ and $\mathrm{M}_{\mathrm{i}-1}=\prod_{j=1}^{i-1} m_{j}$. Coefficient $\mathrm{a}_{\mathrm{i}}$ can be computed using small look up tables. These tables are labeled as $\mathrm{a}_{\mathrm{i}}$. For n moduli set the number of look-up tables required are $n-1$, with a size of $m_{i} \log _{2} m_{i}$, where $m_{i}$ is the $i^{\text {th }}$ moduli. The functional block diagram for calculating $X_{i}$ is shown in figure 1.


Fig. 1: Functional block diagram for $\mathrm{i}^{\text {th }}$ module.

Table index $=\left|r_{i}-X_{i-1}\right|_{m_{i}}$ for $\mathrm{i} \geq 2$ and table content of coefficients are given as;
$\mathrm{a}_{\mathrm{i}}$;such that $\left|r_{i}-X_{i-1}\right|_{m_{i}}=\left|a_{i} . M_{i-1}\right|_{m_{i}} \quad 0 \leq \mathrm{a}_{\mathrm{i}}<\mathrm{m}_{\mathrm{i}}$
Our main goal is to keep the look up tables small and their size should also be small. This could be realized by choosing $\mathrm{m}_{1}$ to be the largest one. Order of other moduli does not affect the memory size.

## B. Implementation of three Moduli Set

Let us implement the concept of 3 moduli set for any bit word length N . The implementation holds good for all the three cases i.e. $|N|_{3}=0,|N|_{3}=1,|N|_{3}=2$. We will discuss for $|N|_{3}=$ 1 in detail. Figure 2 shows the conversion process, where 2 modules are required to calculate the MRC coefficients.


Fig.2: 3 Moduli set

## Implementation.

## For $|N|_{3}=1$

According to formulae for the generation of moduli sets the moduli set is given as
$2^{\mathrm{n}}-1,2^{\mathrm{n}+1}, 2^{\mathrm{n}}+1$ and $\mathrm{N}=3 \mathrm{n}+1$
Again we took $\mathrm{N}=10$. So by using equation (15), n comes out to be 3 and the moduli set should be $7,16,9$. As is the
requirement of MRC II [5], the largest modulus should be designated as $m_{1}$. Thus the moduli set are $m_{1}=16, m_{2}=7$ and $\mathrm{m}_{3}=9$.
In our case X is represented as:
$X=X_{3}=a_{3} m_{1} m_{2}+a_{2} m_{1}+a_{1}$

Vol. 8, Issue 11, ISSN No. 2455-2143, Pages 161-173
Published Online March 2024 in IJEAST (http://www.ijeast.com)
From (14) $X_{1}=a_{1}=r_{1}$ and $M_{1}=m_{1}$.
And
$\left|r_{2}-X_{1}\right|_{m_{2}}=\left|a_{2} \cdot M_{1}\right|_{m_{2}} ;$
Substituting the values of $M_{1}$ and $m_{2}$, equation (17) becomes:

$$
\begin{equation*}
\left|r_{2}-X_{1}\right|_{7}=\left|a_{2 .} .16\right|_{7} \tag{17}
\end{equation*}
$$

In order to calculate the values of $\mathrm{a}_{2}$ to build the look-up table, we need to know all the possible values of $\left|\mathrm{r}_{2}-\mathrm{X}_{1}\right|_{7}$ and in our case the values are $\{0,1,2,3,4,5,6\}$. We do not need to know all the individual values


Fig. 3: MRC II Implementation flow diagram
of $\mathrm{r}_{2}$ or $\mathrm{X}_{1}$. Coefficient $\mathrm{a}_{2}$ is calculated using (18)

$$
\begin{aligned}
& \left|\mathrm{r}_{2}-\mathrm{X}_{1}\right|_{7}=0=\left|\mathrm{a}_{2} .16\right|_{7} \Rightarrow \mathrm{a}_{2}=0 ; \\
& \left|\mathrm{r}_{2}-\mathrm{X}_{1}\right|_{7}=1=\left|\mathrm{a}_{2 .} .16\right|_{7} \Rightarrow>\mathrm{a}_{2}=4
\end{aligned}
$$

Successive values of $\mathrm{a}_{2}$ can be calculated and hence look-up table is constructed as shown in table 1 .

| Table ( $\mathbf{a}_{2}$ ) |  | Table $\left(\mathbf{a}_{3}\right)$ |  |
| :---: | :---: | :---: | :---: |
| $\left\|\mathbf{r}_{2}-\mathbf{X}_{1}\right\| \boldsymbol{7}$ | $\mathbf{a}_{2}$ | $\left\|\mathbf{r}_{3}-\mathbf{X}_{2}\right\| \boldsymbol{a}$ | $\mathbf{a}_{3}$ |
| 0 | 0 | 0 | 0 |
| 1 | 4 | 1 | 7 |
| 2 | 1 | 2 | 5 |
| 3 | 5 | 3 | 3 |
| 4 | 2 | 4 | 1 |
| 5 | 6 | 5 | 8 |
| 6 | 3 | 6 | 6 |
|  |  | 7 | 4 |
|  |  | 8 | 2 |

Table 1: Two tables for moduli set $\left\{\mathrm{m}_{1}, \mathrm{~m}_{2}, \mathrm{~m}_{3}\right\}=16,7,9$
Coefficient $a_{3}$ shown above in the table are calculated in the same manner as coefficient $\mathrm{a}_{2}$ are calculated. For the construction of table ( $\mathrm{a}_{3}$ ):
$\left|r_{3}-X_{2}\right|_{m_{3}}=\quad\left|a_{3} \cdot M_{2}\right|_{m_{3}}$
where $\mathrm{M}_{2}=\mathrm{m}_{1 .} \mathrm{m}_{2}=16 * 7=112$ and $\mathrm{m}_{3}=9$;

$$
0 \leq \mathrm{r}_{3}<\mathrm{m}_{3} \text { and } 0 \leq \mathrm{X}_{2}<\mathrm{M}_{2}
$$

Substituting the values of $\mathrm{M}_{2}$ and $\mathrm{m}_{3}$, equation (19) becomes

$$
\left|\mathrm{r}_{3}-\mathrm{X}_{2}\right|_{9}=\left|\mathrm{a}_{3} .112\right|_{9}
$$

And
$\left|\mathrm{r}_{3}-\mathrm{X}_{2}\right|_{9}=0=\left|\mathrm{a}_{3} .112\right|_{9} \Rightarrow \mathrm{a}_{3}=0$

$$
\begin{equation*}
\left|\mathrm{r}_{3}-\mathrm{X}_{2}\right|_{9}=1=\left|\mathrm{a}_{3} .112\right|_{9} \Rightarrow \mathrm{a}_{3}=7 \tag{19}
\end{equation*}
$$

The complete flow diagram for 3 moduli set implementation is shown in figure 3.Here we have shown the conversion of RNS number $(4,5,1)$ with respect to $\left(m_{1}, m_{2}, m_{3}=16,7,9\right)$ to binary number 964 . From look up table 1 , value of $a_{2}=4$ corresponding to $\left|\mathrm{r}_{2}-\mathrm{X}_{1}\right|_{7}=1$. Using formula from equation
(14) the value of $X_{2}$ comes out to be 68 . Further the value of $a_{3}$ from look up table is 8 corresponding to $\left|r_{3}-X_{2}\right|_{7}=5$. Again using (14) the value of $X_{3}$ is 964 as is shown as the final output in figure 3.
a. Hardware Implementation of MRC II: The look up tables used in our work is all small tables. In 3 moduli set, only two look up tables are required. Further we can see in table 1
that the values of $\left|r_{2}-X_{1}\right|_{7}$ ranges from 0 to 6 , so three bits ( $x$ y z) are required to represent those numbers. Similarly, coefficients of $\mathrm{a}_{2}$ ranges from 0 to 6 , so again three bits ( abc ) are required to represent them as well. As shown below in look up table design xyz and abc are taken as input and output variables respectively. In case of $a_{3}$ there would be 4 input and 4 output variables.

Truth table and Circuit Implementation of $\mathbf{a}_{2}$ :


| Input <br> xyz | Output <br> abc |
| :---: | :---: |
| 000 | 000 |
| 001 | 100 |
| 010 | 001 |
| 011 | 101 |
| 100 | 010 |
| 101 | 110 |
| 110 | 011 |
| 111 | xxx |




Fig.4: Look up Table Design for $\mathrm{a}_{2}$

The outputs of (a), (b) and (c) are:
$\mathrm{a}=\mathrm{X}^{\prime} Z+\mathrm{Y}^{\prime} Z \quad \mathrm{~b}=\mathrm{X} \mathrm{Y}^{\prime}+\mathrm{X} Z^{\prime} \quad \mathrm{c}=\mathrm{X}^{\prime} \mathrm{Y}+\mathrm{Y} Z^{\prime}$
Similarly, we can implement the other two moduli sets i.e. $\mathrm{M}_{0}$ and $\mathrm{M}_{2}$ for remainder zero and remainder 2
respectively. The coefficients $a_{2}$ and $a_{3}$ for the above said moduli set are calculated and shown in the table 2 and 3. Table 2 shows the coefficients for $\mathrm{M}_{2}$ and table 3 is for $\mathrm{M}_{0}$.

Vol. 8, Issue 11, ISSN No. 2455-2143, Pages 161-173
Published Online March 2024 in IJEAST (http://www.ijeast.com)

| Table ( $\mathbf{a}_{2}$ ) |  |  |  |
| :---: | :---: | :---: | :---: |
| $\left\|\mathbf{r}_{2}-\mathbf{X}_{1}\right\| \boldsymbol{7}$ | $\mathbf{a}_{2}$ | Table ( $\left.\mathbf{a}_{3}\right)$ |  |
| $\mid 0$ | 0 | 0 | 0 |
| 1 | 2 | 1 | 8 |
| 2 | 4 | 2 | 7 |
| 3 | 6 | 3 | 6 |
| 4 | 1 | 4 | 5 |
| 5 | 3 | 5 | 4 |
| 6 | 5 | 6 | 3 |
|  |  | 7 | 2 |
|  |  | 8 | 1 |

Table2: Two tables for moduli set $\left\{\mathrm{m}_{1}, \mathrm{~m}_{2}, \mathrm{~m}_{3}\right\}=32,7,9$

| Table ( $\mathbf{a}_{2}$ ) |  |  |  |
| :---: | :---: | :---: | :---: |
| $\left\|\mathbf{r}_{2}-\mathbf{X}_{1}\right\| \mathbf{8}$ | $\mathbf{a}_{2}$ | Table ( $\left.\mathbf{a}_{3}\right)$ |  |
| 0 | 0 | 0 | 0 |
| 1 | 1 | 1 | 4 |
| 2 | 2 | 2 | 1 |
| 3 | 3 | 3 | 5 |
| 4 | 4 | 4 | 2 |
| 5 | 5 | 5 | 6 |
| 6 | 6 | 6 | 3 |
| 7 | 7 |  |  |

Table 3: Two tables for moduli set $\left\{\mathrm{m}_{1}, \mathrm{~m}_{2}, \mathrm{~m}_{3}\right\}=9,8,7$

## C. RNS to Binary Converter using CRT.

In this section we will implement our three-modulus set using CRT [6]. Again, we have three cases i.e. we have one modulus set in which N is exactly divisible by 3 and the other two where we get remainder 1 or 2 when N is not exactly divisible by 3 . In this implementation we will discuss one case where we get remainder 1 when N is divided by 3 .
$\boldsymbol{F o r}|N|_{3}=1$
The moduli set in this case is $\mathrm{M}_{1}=\left(\mathrm{m}_{1}, \mathrm{~m}_{2}, \mathrm{~m}_{3}\right)=\left(2^{\mathrm{n}+1}, 2^{\mathrm{n}}-1\right.$, $\left.2^{\mathrm{n}}+1\right)$. Nonnegative number $\mathrm{X}=\left(\mathrm{x}_{1}, \mathrm{x}_{2}, \mathrm{x}_{3},\right)$ where $\mathrm{X}_{\mathrm{i}}=|X|_{m_{i}}$ and $i$ ranges from 1 to 3 . The dynamic range is:
$\mathrm{M}=\prod_{i=1}^{3} m_{i}=\left(2^{\mathrm{n}+1}\right)^{*}\left(2^{\mathrm{n}}-1\right)^{*}\left(2^{\mathrm{n}}+1\right)$
According to Chinese remainder theorem (CRT) number X is defined as:
$\mathrm{X}=\sum_{i=1}^{N} \widehat{m}_{i}\left\langle\frac{1}{\widehat{m}_{i}}\right\rangle_{m_{i}} \mathrm{X}_{\mathrm{i}}-\mathrm{MN}(\mathrm{X})$
For 3 moduli set CRT equation is reduced to:
$\mathrm{X}=\widehat{m}_{1}\left\langle\frac{1}{\widehat{m}_{1}}\right\rangle_{m_{1}} X_{1}+\widehat{m}_{2}\left\langle\frac{1}{\widehat{m}_{2}}\right\rangle_{m_{2}} X_{2}+\widehat{m}_{3}\left\langle\frac{1}{\widehat{m}_{3}}\right\rangle_{m_{3}} X_{3}-\mathrm{MN}(\mathrm{X})$
(22)
where $\widehat{m}_{\mathrm{I}}=\frac{M}{m_{i}} ;\left\langle\frac{1}{\widehat{m}_{i}}\right\rangle_{m_{i}}$ is multiplicative inverse of $\widehat{m}_{\mathrm{i}}$ and $\mathrm{N}(\mathrm{X})$ is an integer.

- $\widehat{\boldsymbol{m}}_{\boldsymbol{i}}$ is defined as the ratio of Dynamic range M and $\mathrm{i}^{\text {th }}$ Moduli set [6].where i range from 1 to 3 .
Consequently $\widehat{m}_{1}=\left(2^{\mathrm{n}}-1\right)^{*}\left(2^{\mathrm{n}}+1\right)=\left(2^{2 \mathrm{n}}-1\right) ; \widehat{m}_{2}=\left(2^{\mathrm{n}+1}\right)^{*}$ $\left(2^{\mathrm{n}}+1\right)^{\prime}$ and $\widehat{m}_{3}=\left(2^{\mathrm{n+1}}\right) *\left(2^{\mathrm{n}}-1\right)$

Vol. 8, Issue 11, ISSN No. 2455-2143, Pages 161-173
Published Online March 2024 in IJEAST (http://www.ijeast.com)

- Multiplicative inverses $\left\langle\frac{1}{\hat{m}_{i}}\right\rangle_{m_{i}}$ are given in the following theorem:

Theorem 2: For the moduli set $\left(\mathrm{m}_{1}, \mathrm{~m}_{2}, \mathrm{~m}_{3}\right)=\left(2^{\mathrm{n}+1}, 2^{\mathrm{n}}-1,2^{\mathrm{n}}\right.$ +1 )the multiplicative inverses are:
$\left\langle\frac{1}{\widehat{m}_{1}}\right\rangle_{m_{1}}=\langle-1\rangle_{m_{1}}$
$\left\langle\frac{1}{\widehat{m}_{2}}\right\rangle_{m_{2}}=\left\langle 2^{n-2}\right\rangle_{m_{2}}$
And $\left\langle\frac{1}{\widehat{m}_{3}}\right\rangle_{m_{3}}=\left\langle-2^{n-2}\right\rangle_{m_{3}}$
Proof: We will show that for $\mathrm{i}=(1,2,3)\left\langle\left\langle\widehat{m_{i}}\right\rangle_{m_{i}}\left\langle\frac{1}{\widehat{m}_{i}}\right\rangle_{m_{i}}\right\rangle_{m_{i}}=1$
For $\mathrm{i}=1 ;\left\langle\widehat{m}_{1}\right\rangle_{m_{1}}=\left\langle 2^{2 n}-1\right\rangle_{2^{n+1}}=\langle-1\rangle_{2^{n+1}}$

$$
\begin{aligned}
& \left\langle\frac{1}{\hat{m}_{1}}\right\rangle_{m_{1}}=\langle-1\rangle_{2^{n+1}} \\
& \left\langle\left\langle\widehat{m}_{1}\right\rangle_{m_{1}}\left\langle\frac{1}{\hat{m}_{1}}\right\rangle_{m_{1}}\right\rangle_{m_{1}}=\langle(-1)(-1)\rangle_{2^{n+1}}=1
\end{aligned}
$$

Fori $=2$

$$
\begin{aligned}
& \left\langle\widehat{m}_{2}\right\rangle_{m_{2}}=\left\langle\left(2^{n+1}\right)\left(2^{n}+1\right)\right\rangle_{2^{n}-1}=\langle 4\rangle_{2^{n}-1} \\
& \left\langle\frac{1}{\widehat{m}_{2}}\right\rangle_{m_{2}}=\left\langle 2^{n-2}\right\rangle_{2^{n}-1} \\
& \left\langle\left\langle\widehat{m}_{2}\right\rangle_{m_{2}}\left\langle\frac{1}{\widehat{m}_{2}}\right\rangle_{m_{2}}\right\rangle_{m_{2}}=\left\langle(4)\left(2^{n-2}\right)\right\rangle_{2^{n}-1} \\
& =\left\langle\left(2^{n}\right)\right\rangle_{2^{n}-1}=\left\langle\left(2^{n}+1-1\right)\right\rangle_{2^{n}-1}=1
\end{aligned}
$$

For $\mathrm{i}=3$

$$
\left\langle\widehat{m}_{3}\right\rangle_{m_{3}}=\left\langle\left(2^{n+1}\right)\left(2^{n}-1\right)\right\rangle_{2^{n}+1}=\langle 4\rangle_{2^{n}+1}
$$

$\left\langle\frac{1}{\widehat{m}_{3}}\right\rangle_{m_{3}}=\left\langle-2^{n-2}\right\rangle_{2^{n}+1}$

$$
\begin{align*}
& \left\langle\left\langle\widehat{m}_{3}\right\rangle_{m_{3}}\left\langle\frac{1}{\widehat{m}_{3}}\right\rangle_{m_{3}}\right\rangle_{m_{3}}=\left\langle(4)\left(-2^{n-2}\right)\right\rangle_{2^{n}+1}  \tag{26}\\
& =\left\langle\left(-2^{n}\right)\right\rangle_{2^{n}+1}=\left\langle\left(2^{n}+1-2^{n}\right)\right\rangle_{2^{n}+1}=1
\end{align*}
$$

We have all the parameters that are required in equation (22).Substituting we get
$\mathrm{X}=-\left(2^{2 \mathrm{n}}-1\right) X_{1}+\left[\left(2^{\mathrm{n}+1}\right)\left(2^{\mathrm{n}}+1\right)\left(2^{n-2}\right)\right] X_{2}+\left[\left(2^{\mathrm{n}+1}\right)\left(2^{\mathrm{n}}-\right.\right.$ 1) $\left.\left(-2^{n-2}\right)\right]-\left(2^{n+1}\right)\left(2^{n}-1\right)\left(2^{n}+1\right) N(X)$

Divide equation (27) by $2^{\text {n+1 }}$ and take the floor value of both sides.
$\left\lfloor\frac{x}{2^{n+1}}\right\rfloor=-\left[2^{n-1}\right] X_{1}+\left\lfloor 2^{2 n-2}+2^{n-2}\right] X_{2}+\left\lfloor-2^{2 n-2}+\right.$
$\left.2^{n-2}\right] X_{3}$ $-\left(2^{2 \mathrm{n}}-1\right) \mathrm{N}(\mathrm{X})(28)$
In order to eliminate the term $\left(2^{2 n}-1\right) \mathrm{N}(\mathrm{X})$ take modulus $2^{2 n}-1$ both sides of (28)
$\left|\frac{x}{2^{n+1}}\right|=\left\langle-2^{n-1} X_{1}\right\rangle_{2^{2 n-1}}+\left\langle\left(2^{2 n-2}+2^{n-2}\right) X_{2}\right\rangle_{2^{2 n-1}}$
$+\left\langle\left(-2^{2 n-2}+2^{n-2}\right) X_{3}\right\rangle_{2^{2 n-1}}(29)$
X is represented by equation:
$X=\left\lfloor\frac{X}{2^{n+1}}\right\rfloor 2^{n+1}+X_{1}$
Equation (29) is fractionated in parts as $u_{1,} u_{2}$ and $u_{3}$ and is expressed as:

$$
\begin{equation*}
\left\lfloor\frac{x}{2^{n+1}}\right\rfloor=\left\langle u_{1}+u_{2}+u_{3}\right\rangle_{2^{2 n-1}} \tag{31}
\end{equation*}
$$

where $u_{1}=\left\langle-2^{n-1} X_{1}\right\rangle_{2^{2 n-1}}$;
$u_{2}=\left\langle\left(2^{2 n-2}+2^{n-2}\right) X_{2}\right\rangle_{2^{2 n-1}}$
$u_{3}=\left\langle\left(-2^{2 n-2}+2^{n-2}\right) X_{3}\right\rangle_{2^{2 n-1}}$
In order to realize (31) we need to obtain the simplified expression of $u_{1}, u_{2}$ and $u_{3}$ by taking modulus of respective terms.

Evaluation of $\mathbf{u}_{1}, \boldsymbol{u}_{2}$ and $\boldsymbol{u}_{3}$ : For the Binary representation of $\mathrm{X}_{\mathrm{i}}, x_{i j}$ represents $\mathrm{j}^{\text {th }}$ bit and its weight is expressed as $2^{\mathrm{j}}$. The complement is given as $\bar{x}_{i j}$.

- Evaluating $\boldsymbol{u}_{\mathbf{1}}: \mathrm{X}_{1}$ is $\mathrm{n}+1$ bit and its 2 n bit representation is:

$$
\begin{gathered}
X_{1}=\overbrace{00 \ldots 00}^{\mathrm{n}-1} \overbrace{x_{1 n} x_{1(n-1)} \ldots . x_{11} x_{10}}^{\mathrm{n}+1} \\
-X_{1}=\overbrace{11 \ldots 11}^{\mathrm{n}-1} \overbrace{\bar{x}_{1 n} \bar{x}_{1(n-1)} \ldots \bar{x}_{11} \bar{x}_{10}}^{\mathrm{n}+1}
\end{gathered}
$$

For $u_{1}$ multiply above equation with $2^{\mathrm{n}-1}$ and take modulus $2^{2 \mathrm{n}}$ -1 , so that equation looks like:
$u_{1}=\left\langle-2^{n-1} X_{1}\right\rangle_{2}{ }^{2 n}-1=$
$\overbrace{\bar{x}_{1 n} \bar{x}_{1(n-1)} \ldots \bar{x}_{11} \bar{x}_{10}}^{\mathrm{n}+1} \overbrace{11 \ldots 11}^{\mathrm{n}-1}(32)$

- Evaluating $\boldsymbol{u}_{2}: \mathrm{X}_{2}$ is n bit. For simplification we split $\mathrm{u}_{2}$ into two parts;
$\boldsymbol{u}_{\mathbf{2}}^{\prime}=\left\langle 2^{2 n-2} X_{2}\right\rangle_{2^{2 n-1}} \quad$ and $\quad \boldsymbol{u}^{\prime \prime}{ }_{2}=\left\langle 2^{n-2} X_{2}\right\rangle_{2^{2 n-1}} 2 n \quad$ bit representation of $X_{2}$ is:
$X_{2}=\overbrace{00 \ldots 00} \overbrace{x_{2(n-1)} x_{2(n-2)} \ldots x_{21} x_{20}}$
$\boldsymbol{u}^{\prime}{ }_{2}=\left\langle 2^{2 n-2} X_{2}\right\rangle_{2^{2 n-1}}=$

$$
\overbrace{x_{21} x_{20} 00 \ldots}^{\mathrm{n}} \overbrace{00 x_{2(n-1)} x_{2(n-2)} \cdots \cdots}^{\mathrm{n}}
$$

$$
\overbrace{00 x_{2(n-1)} x_{2(n-2)} \cdots{ }^{\boldsymbol{u}_{2}^{\prime \prime}=\left\langle 2^{n-2} X_{2}\right\rangle_{2 n-1}=}} \overbrace{x_{21} x_{20} 00 \ldots}^{\mathrm{n}} \underbrace{\mathrm{n}}
$$

n


Fig. 5: Functional Block Diagram
$u_{2}=\boldsymbol{u}_{2}^{\prime}+\boldsymbol{u}^{\prime \prime}{ }_{2}=\left\langle\left(2^{2 n-2}+2^{n-2}\right) X_{2}\right\rangle_{2^{2 n-1}}$
$u_{2}=$
$\overbrace{x_{21} x_{20} x_{2(n-1)}} x_{2(n-2)} \cdots \overbrace{x_{21} x_{20} x_{2(n-1)} x_{2(n-2)} \cdots}$

- Evaluating $u_{3}: X_{3}$ is $n+1$ bit. Again, for simplification we split $\mathrm{u}_{3}$ in two parts;
$\boldsymbol{u}^{\prime}{ }_{3}=\left\langle-2^{2 n-2} X_{3}\right\rangle_{2^{2 n-1}}$ and $\boldsymbol{u}^{\prime \prime}{ }_{3}=\left\langle 2^{n-2} X_{3}\right\rangle_{2^{2 n-1}}$
2 n bit representation of $X_{3}$ is:

$$
\begin{gathered}
\begin{array}{c}
X_{3}=\overbrace{00 \ldots 00}^{n-1} \overbrace{x_{3 n} x_{3(n-1)}}^{\mathrm{n}+1} \ldots x_{31} x_{30} \\
\mathrm{n}+1
\end{array} \\
-X_{3}=\overbrace{11 \ldots 11}^{\mathrm{n}+1} \overbrace{\bar{x}_{3 n} \bar{x}_{3(n-1)} \ldots \bar{x}_{31} \bar{x}_{30}}
\end{gathered}
$$

$$
\begin{align*}
& \boldsymbol{u}_{3}^{\prime}=\left\langle-2^{2 n-2} X_{3}\right\rangle_{2^{2 n-1}}= \\
& \overbrace{\bar{x}_{31} \bar{x}_{30}}^{11 \ldots . \overbrace{11 \bar{x}_{3 n} \bar{x}_{3(n-1)} \ldots}}  \tag{36}\\
& \boldsymbol{u}_{3}^{\prime \prime}=\left\langle 2^{n-2} X_{3}\right\rangle_{2^{2 n-1}}= \\
& \overbrace{0 x_{3 n} x_{3(n-1)} \ldots} \overbrace{x_{32} x_{31} x_{30} 00 \ldots 0} \tag{37}
\end{align*}
$$

$$
u_{3}=\boldsymbol{u}_{3}^{\prime}+\boldsymbol{u}^{\prime \prime}{ }_{3}=\left\langle\left(-2^{2 n-2}+2^{n-2}\right) X_{3}\right\rangle_{2^{2 n-1}}
$$

In $u_{3}$, both $\boldsymbol{u}_{3}^{\prime}$ and $\boldsymbol{u}^{\prime \prime}{ }_{3}$ are 2 n bits and are overlapping. Thus we took $\boldsymbol{u}_{3}$ and $\boldsymbol{u}^{\prime \prime}{ }_{3}$ separately. Now we have simplified $u_{1}, u_{2}$, $u_{3}, u_{3}$ so that these parameters could be incorporated in equation (31) for the realization. Eventually we need to get the value of X from equation (30). The parameter $\left\lfloor\frac{X}{2^{n+1}}\right\rfloor$ is multiplied by $2^{n+1}$ or a shifting $\left\lfloor\frac{x}{2^{n+1}}\right\rfloor$ by $\mathrm{n}+1$ bits and adding $\mathrm{X}_{1}$, an $\mathrm{n}+1$ bit number. The complete functional block

Vol. 8, Issue 11, ISSN No. 2455-2143, Pages 161-173
Published Online March 2024 in IJEAST (http://www.ijeast.com)
Table 4: Expression for $\mathrm{U}_{1}, \mathrm{U}_{2}$ and $\mathrm{U}_{3}$

| $\|\boldsymbol{N}\|_{3}=\mathbf{0}$ | $\|\boldsymbol{N}\|_{\mathbf{3}}=\mathbf{1}$ | $\|\boldsymbol{N}\|_{3}=\mathbf{2}$ |
| :--- | :--- | :--- |
| $u_{1}=\left\langle-2^{n} X_{1}\right\rangle_{2^{2 n-1}}$ | $u_{1}=\left\langle-2^{n-1} X_{1}\right\rangle_{2^{2 n-1}}$ | $u_{1}=\left\langle-2^{n-2} X_{1}\right\rangle_{2^{2 n-1}}$ |
| $u_{2}=\left\langle\left(2^{2 n-1}+2^{n-1}\right) X_{2}\right\rangle_{2^{2 n-1}}$ | $u_{2}=\left\langle\left(2^{2 n-2}+2^{n-2}\right) X_{2}\right\rangle_{2^{2 n-1}}$ | $u_{2}=\left\langle\left(2^{2 n-3}+2^{n-3}\right) X_{2}\right\rangle_{2^{2 n-1}}$ |
| $u_{3}=\left\langle\left(-2^{2 n-1}+2^{n-1}\right) X_{3}\right\rangle_{2^{2 n-1}}$ | $u_{3}=\left\langle\left(-2^{2 n-2}+2^{n-2}\right) X_{3}\right\rangle_{2^{2 n-1}}$ | $u_{3}=\left\langle\left(-2^{2 n-3}+2^{n-3}\right) X_{3}\right\rangle_{2^{2 n-1}}$ |



Fig. 6: Implementation of 3 modulii set for $\mathrm{M}_{1}$
diagram is shown below in figure 5, where $\mathrm{W}=\left\lfloor\left.\frac{x}{2^{n+1}} \right\rvert\,\right.$. The computation of X is achieved by wiring operations, eliminating the need for multiplication and addition operations.

On similar lines we can drive expressions for $\mathrm{u}_{1}, \mathrm{u}_{2}$ and $\mathrm{u}_{3}$ for $|N|_{3}=0$ and $|N|_{3}=2$. Table 4 shows $\mathrm{u}_{1}, \mathrm{u}_{2}$ and $\mathrm{u}_{3}$ for $|N|_{3}=0$, $|N|_{3}=1$ and $|N|_{3}=2$.

The following example elaborates the case for $\mathrm{N}=10$; $\mathrm{i}=$ $|10|_{3}=1$ and $\mathrm{n}=3$.

Example 4.1: For the Modulus set (16, 7, 9), convert the residue representation $(4,5,1)$ into its binary equivalent. From equation (31) we need to calculate $u_{1}, u_{2}$ and $u_{3}$. $X_{1}=4 ; X_{1}=X_{13} X_{12} X_{11} X_{10}=0100$, thus from equation (32), the value of $\mathrm{u}_{1}$ is given as:

$$
\mathrm{u}_{1}=\bar{X}_{13} \bar{X}_{12} \bar{X}_{11} \bar{X}_{10} \mid 11=101111=47
$$

$\mathrm{X}_{2}=5 ; \mathrm{X}_{2}=\mathrm{X}_{22} \mathrm{X}_{21} \mathrm{X}_{20}=101$, from equation (35), the value of $u_{2}$ is given as:

$$
\mathrm{u}_{2}=X_{21} X_{20} X_{22} \mid X_{21} X_{20} X_{22}=011011=
$$

27
$X_{3}=1 ; X_{3}=X_{33} X_{32} X_{31} X_{30}=000$ 1, from equation (36), (37), the value of $u_{3}$ and $u_{3}$ " is:

Table 5: Binary form values of $\mathrm{U}_{1}, \mathrm{U}_{2}$ and $\mathrm{U}_{3}$ for $|N|_{3}=0,|N|_{3}=1$ and $|N|_{3}=2$

| Case <br> Notation | $\|N\|_{3}=0$ | $\|N\|_{3}=1$ | $\|N\|_{3}=2$ |
| :---: | :---: | :---: | :---: |
| For $\mathrm{U}_{1}$ | $\mathrm{X}_{1}$ is n bit $\begin{gathered} \mathrm{X}_{1}=x_{1(n-1)} x_{1(n-2)} \ldots x_{11} x_{10} \\ \mathrm{U}_{1}=\bar{x}_{1(n-1)} \bar{x}_{1(n-2) .} . . \bar{x}_{11} \bar{x}_{10} \\ \mid 11 \ldots 11 \end{gathered}$ | $\mathrm{X}_{1}$ is $\mathrm{n}+1$ bit $\begin{gathered} \mathrm{X}_{1}=x_{1 n} x_{1(n-1)} \ldots x_{11} x_{10} \\ \mathrm{U}_{1}=\bar{x}_{1 n} \bar{x}_{1(n-1)} . . \bar{x}_{11} \bar{x}_{10} \\ \mid 11 \ldots 11 \end{gathered}$ | $\mathrm{X}_{1}$ is $\mathrm{n}+2$ bit $\begin{gathered} \mathrm{X}_{1}=x_{1(n+1)} x_{1 n} \ldots x_{11} x_{10} \\ \mathrm{U}_{1}=\bar{x}_{1(n+1)} \bar{x}_{1 n} . . \bar{x}_{11} \bar{x}_{10} \\ \mid 11 \ldots 11 \end{gathered}$ |
| For $\mathrm{U}_{2}$ | $\mathrm{X}_{2}$ is n bit $\begin{gathered} \mathrm{X}_{2}=x_{2(n-1)} x_{2(n-2)} \ldots x_{21} x_{20} \\ \mathrm{U}_{2}=x_{20} x_{2(n-1)} x_{2(n-2)} \ldots x_{21} \\ \quad \mid x_{20} x_{2(n-1)} x_{2(n-2)} \ldots x_{21} \end{gathered}$ | $\mathrm{X}_{2}$ is n bit $\begin{aligned} \mathrm{X}_{2}= & x_{2(n-1)} x_{2(n-2)} \cdots x_{21} x_{20} \\ \mathrm{U}_{2}= & x_{21} x_{20} x_{2(n-1)} x_{2(n-2)} \cdots \\ & \mid x_{21} x_{20} x_{2(n-1)} x_{2(n-2)} \cdots \end{aligned}$ | $\mathrm{X}_{2}$ is n bit $\begin{aligned} & \mathrm{X}_{2}=x_{2(n-1)} \ldots x_{22} x_{21} x_{20} \\ & \mathrm{U}_{2}= x_{22} x_{21} x_{20} x_{2(n-1)} \cdots \\ & \quad \mid x_{22} x_{21} x_{20} x_{2(n-1)} \cdots \end{aligned}$ |
| $\begin{aligned} & \text { For } \mathrm{U}_{3} \\ & \mathrm{U}_{3}+\mathrm{U}_{3}{ }^{\prime \prime} \end{aligned}$ | $\mathrm{X}_{3}$ is $\mathrm{n}+1$ bit $\begin{aligned} & \mathrm{X}_{3}=x_{3 n} x_{3(n-1)} \ldots x_{31} x_{30} \\ & \mathrm{U}_{3}^{\prime}=\bar{x}_{30} 11 \ldots 1 \mid \\ & \quad 1 \bar{x}_{3 n} \bar{x}_{3(n-1)} . . \bar{x}_{31} \\ & \mathrm{U}_{3}{ }^{\prime \prime}=x_{3 n} x_{3(n-1)} \ldots x_{31} \mid x_{30} \\ & \quad 00 \ldots 00 \end{aligned}$ | $\mathrm{X}_{3}$ is $\mathrm{n}+1$ bit $\begin{gathered} \mathrm{X}_{3}=x_{3 n} x_{3(n-1)} \ldots x_{31} x_{30} \\ \mathrm{U}_{3}^{\prime}=\bar{x}_{31} \bar{x}_{30} 11 \ldots \mid \\ 11 \bar{x}_{3 n} \bar{x}_{3(n-1)} \ldots \\ \mathrm{U}_{3}^{\prime \prime}=0 x_{3 n} x_{3(n-1)} \ldots \mid x_{32} x_{31} \\ x_{30} 00 \ldots 0 \end{gathered}$ | $\mathrm{X}_{3}$ is $\mathrm{n}+1$ bit $\begin{aligned} & \mathrm{X}_{3}=x_{3 n} \ldots x_{32} x_{31} x_{30} \\ & \mathrm{U}_{3}=\bar{x}_{32} \bar{x}_{31} \bar{x}_{30} 1 \ldots \mid \\ & 111 \bar{x}_{3 n} \ldots \\ & \mathrm{U}_{3}^{\prime \prime}=00 x_{3 n} \ldots \mid \\ & \quad x_{33} x_{32} x_{31} x_{30} \ldots \ldots 0 \end{aligned}$ |

Table 6: Time Delay and Hardware Complexity Comparison

| Converter | Moduli Set ${ }^{\star}$ | FA | HA | AND/OR | XOR/NOR | Other | Delay |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| [19] | $\mathrm{M}_{3}$ | 6n-1 | $3 \mathrm{n}-7$ | --- | ---- | ( $\mathrm{n}-1$ )MUX | (4n) $t_{\text {FA }}$ |
| [15] | $\mathrm{M}_{0}$ | $6 \mathrm{n}+1$ | ---- | $\mathrm{n}+3$ | $\mathrm{n}+1$ | 2n MUX | $(2 \mathrm{n}+2) \mathrm{t}_{\mathrm{FA}}+\mathrm{t}_{\text {M }} \mathrm{CUX}$ |
| [17,18] | $\mathrm{M}_{0}$ | 4n | ---- | 2 | ---- | -- | $(4 \mathrm{n}+1) \mathrm{t}_{\text {FA }}$ |
| [16] CI | $\mathrm{M}_{0}$ | 4n | 1 | --- | 1 | 2 MUX | $(4 \mathrm{n}+1) \mathrm{t}_{\text {FA }}$ |
| [16] CII | $\mathrm{M}_{0}$ | 6n | 1 | 1 | 1 | ( $2 \mathrm{n}+2$ ) MUX | $(\mathrm{n}+1) \mathrm{t}_{\mathrm{FA}}$ |
| [16] CIII | $\mathrm{M}_{0}$ | 4 n | 1 | $2 \mathrm{n}+2$ | $2 \mathrm{n}-1$ | ( $2 \mathrm{n}+2$ ) MUX | $(\mathrm{n}+1) \mathrm{t}_{\text {FA }}$ |
| [14] CI | $\mathrm{M}_{4}$ | $4 \mathrm{n}+3$ | ---- | N | n | --- | $(6 \mathrm{n}+5) \mathrm{t}_{\text {FA }}$ |
| [14] CII | $\mathrm{M}_{4}$ | $14 \mathrm{n}+21$ | $2 \mathrm{n}+3$ | ---- | ---- | (2n+1)3:1MUX | $(2 \mathrm{n}+7) \mathrm{t}_{\text {FA }}$ |
| [14] CIII | $\mathrm{M}_{4}$ | $12 \mathrm{n}+19$ | $2 \mathrm{n}+2$ | ---- | ---- | $\begin{aligned} & 10(2 \mathrm{n}+1) \mathrm{A}_{\mathrm{ROM}} \\ & (2 \mathrm{n}+2) 2: 1 \mathrm{MUX} \end{aligned}$ | (2n+7) $\mathrm{t}_{\text {FA }}$ |
| Ours | $\begin{array}{ll} \hline \mathrm{M}_{0,} & \mathrm{M}_{1}, \\ \mathrm{M}_{2} & \\ \hline \end{array}$ | 6n | ---- | ---- | -- | $\begin{aligned} & \text { 2n }+1+\text { i } \\ & \text { Inverters } \end{aligned}$ | $(2 n+2) t_{\text {FA }}$ |

$* M_{0}=\left\{2^{n}-1,2^{n}, 2^{n}+1\right\}, M_{1}=\left\{2^{n}-1,2^{n+1}, 2^{n}+1\right\}, M_{2}=\left\{2^{n}-1,2^{n+2}, 2^{n}+1\right\}, M_{3}=\left\{2^{n}-1,2^{n}, 2^{n-1}-1\right\}$ and $M_{4}=\left\{2^{n+1}-1\right.$, $\left.2^{\mathrm{n}}, 2^{\mathrm{n}}-1\right\}$.

Table 7: Comparison of various Converters for Three Moduli set in terms of Dynamic Range Efficiency (Deff)

| Word Length (N) | Value of $n$ | Mouldi Set | $\mathrm{D}_{\text {eff }}=\mathbf{M} / \mathbf{2}^{\mathbf{N}}$ |
| :---: | :---: | :---: | :---: |
|  |  | $\mathrm{M}_{0}$ | 99\% |
| $\|\mathrm{N}\|_{3}=0$ | $\mathrm{N}=3 \mathrm{n}$ | $\mathrm{M}_{1}$ | 200\% |
|  |  | $\mathrm{M}_{2}$ | 393\% |
|  |  | $\begin{aligned} & \mathrm{M}_{3} \\ & \mathrm{M}_{4} \end{aligned}$ | $\begin{aligned} & 33 \% \\ & -164 \% \end{aligned}$ |
|  |  | $\mathrm{M}_{0}$ | 50\% |
| $\|\mathrm{N}\|_{3}=1$ | $\mathrm{N}=3 \mathrm{n}+1$ | $\mathrm{M}_{1}$ | 99\% |
|  |  | $\mathrm{M}_{2}$ | 197\% |
|  |  | $\mathrm{M}_{3}$ | 17\% |
|  |  | $\mathrm{M}_{4}$ | 82\% |
| $\|\mathrm{N}\|_{3}=2$ | $\mathrm{N}=3 \mathrm{n}+2$ | $\mathrm{M}_{0}$ | 25\% |
|  |  | $\mathrm{M}_{1}$ | 50\% |
|  |  | $\mathrm{M}_{2}$ | 99\% |
|  |  | $\mathrm{M}_{3}$ | 8\% |
|  |  | $\mathrm{M}_{4}$ | 41\% |

$\mathrm{u}_{3}{ }^{\prime}=\overline{\mathrm{X}}_{31} \overline{\mathrm{X}}_{30} \mid 11 \overline{\mathrm{X}}_{33} \overline{\mathrm{X}}_{32}=101111$

And $u_{3}{ }^{\prime \prime}=0 X_{33} \mid X_{32} X_{31} X_{30} 0=000010$
Again, from the assumption that $\left\lfloor\frac{\mathrm{x}}{2^{\mathrm{n}+1}}\right\rfloor=\mathrm{W}$ and following equation (31),
$\left\lfloor\frac{\mathrm{x}}{2^{\mathrm{n}+1}}\right\rfloor=\mathrm{W}=\left\langle\mathrm{u}_{1}+\mathrm{u}_{2}+\mathrm{u}_{3}\right\rangle_{2^{2 \mathrm{n}-1}}$

Here we can see that X is obtained by shifting $\mathrm{W}, \mathrm{n}+1$ bit to the left and appending $X_{1}$. Figure 6 shows the implementation of this example.

Theorem 3: For any word length N , the three moduli set is $\left\{2^{\mathrm{n+i}}, 2^{\mathrm{n}}-1,2^{\mathrm{n}}+1\right\}=\left\{\mathrm{m}_{1}, \mathrm{~m}_{2}, \mathrm{~m}_{3}\right\}$, where $\mathrm{i}=|\mathrm{N}|_{3}$. Any integer X in $[0, \mathrm{M}-1]$ has RNS representation of $\left(\mathrm{x}_{1}, \mathrm{x}_{2}, \mathrm{x}_{3}\right)$, where $X_{j}=|X|_{m_{j}}$ and the residue size is $(\mathrm{n}+\mathrm{i}, \mathrm{n}, \mathrm{n}+1)$ respectively. The Value of X can be computed from $\mathrm{X}=\mathrm{W} * 2^{\mathrm{n}+\mathrm{i}}+\mathrm{X}_{1}$

Where $W=\left\langle u_{1}+u_{2}+u_{3}\right\rangle_{2^{2 n-1}}$ and $u_{j}=f\left\langle x_{j}\right\rangle$ in $2 n$ bit representation. The value of $u_{j}$ is summarized in table 5 for each $i$. The size of $X$ is $3 n+i$.

## Hardware Architecture:

Theorem 3 states that regardless of $i$, the hardware architecture is the same. It consists of rewiring each residue $\mathrm{x}_{\mathrm{j}}$ to compute $u_{j}$ in $2 n$ bit representation. The $u_{j}$ 's are then added $\bmod \left(2^{2 n}-\right.$ 1) to compute $W$. The final answer $X$ is computed by shifting W by $\mathrm{n}+\mathrm{i}$ bit and appending $\mathrm{X}_{1}$ of size $\mathrm{n}+\mathrm{i}$ bit, i.e. no overlapped. The hardware architecture is shown in figure 5 regardless of i. The only hardware to be implemented is the $\bmod \left(2^{2 n}-1\right)$ adder, which is shown in figure 7. The propagation delay and hardware count are determined by the $\bmod \left(2^{2 n}-1\right)$ adder. The wiring operations only require inverters and introduce one inverter delay. The number of inverters required is determined when computing $u_{j} . u_{1}$ needs to compute $-\mathrm{X}_{1}$, which requires $\mathrm{n}+1$ (size of $\mathrm{X}_{1}$ ) inverters; $\mathrm{u}_{2}$ does not require any inverter and $u_{3}$ needs to compute $-X_{3}$ which requires $n+1$ (size of $X_{3}$ ) inverters i.e. total number of inverters is $n+1+\mathrm{i}$, where $\mathrm{i}=|\mathrm{N}|_{3}$.
The mod adder architecture consists of 2 CSA (2n) and each CSA ( 2 n ) contribute 1 FA delay. The 2 operands $\bmod \left(2^{2 \mathrm{n}}-1\right)$ adder is implemented using RCA (2n) which contributes $2 n$

FA delay. The 1 inverter delay is insignificant compared to FA delay and thus the total delay is $2 n+2$. Each CSA ( $2 n$ ) has $2 n$

FA and RCA (2n) has 2n FA, therefore a total of 6n FAs are used. The mod adder architecture is shown in figure 7.


Fig. 7: Architecture of $\bmod 2^{2 n}-1$ adder

## v. COMPARISION

In table 6, the comparison is done in terms of delay time and table 7 did the comparison in terms of dynamic range [2023].Looking at table 6 we can say that in our case we are using only 6 n full adders and $2 \mathrm{n}+1+\mathrm{i}$ inverters, whereas all other converters require the use of one or more components such as gates (OR, NAND, XOR, NOR), Half Adders and Multiplexers in addition to Full adders, thus reducing the hardware complexity to its minimum threshold value. If we look at the delay time, we are superior to all the converters except CII, CIII [16].
In table 7, we compare the Dynamic Range Efficiency ( $\mathrm{D}_{\text {eff }}$ ) of the 5 Moduli sets with respect to word length N . The best $\mathrm{D}_{\text {eff }}$ of each moduli set is as follows:
$\mathrm{M}_{0} \quad \mathrm{D}_{\text {eff }}=99 \%$ if $|\mathrm{N}|_{3}=0$
$\mathrm{M}_{1} \quad \mathrm{D}_{\text {eff }}=99 \%$ if $|\mathrm{N}|_{3}=1$
$\mathrm{M}_{2} \quad \mathrm{D}_{\text {eff }}=99 \%$ if $|\mathrm{N}|_{3}=2$
$\mathrm{M}_{3} \quad \mathrm{D}_{\text {eff }}=33 \%$ if $|\mathrm{N}|_{3}=0$
$\mathrm{M}_{4} \quad \mathrm{D}_{\text {eff }}=82 \%$ if $|\mathrm{N}|_{3}=1$
Thus, we can say that our proposed moduli set $\mathrm{M}_{0}, \mathrm{M}_{1}$ and $\mathrm{M}_{2}$ are the most efficient as far as $\mathrm{D}_{\text {eff }}$ is concerned.
The proposed Moduli set $\mathrm{M}_{0}, \mathrm{M}_{1}$ and $\mathrm{M}_{2}$ have the following properties.

1. Excellent Propagation Delay.
2. Simple Hardware Architecture.
3. The Best $D_{\text {eff. }}$.
vi. CONCLUSION

In this paper we proposed a new three moduli set ( $2^{\mathrm{n}}-1,2^{\mathrm{nti}}$, $2^{\mathrm{n}}+1$ ), where $\mathrm{i}=|\mathrm{N}|_{3}$. It is shown to have the best Dynamic Range Efficiency for any N. The CRT implementation has the same architecture for any i. It consists of wiring operations and one mod adder operations. The Hardware complexity is $6 n$ FA, $2 n+1+\mathrm{i}$ inverters with competitive propagation delay of $(2 n+2) \mathrm{t}_{\mathrm{FA}}$.

## VII. REFERENCES

[1]. N.S. Szabo, R.I. Tanaka, Residue Arithmetic and its Application to Computer Technology, McGraw-Hill, New York, 1967.
[2]. [2] M. Soderstrand, W. Jenkins, G. Jullien, F. Taylor (Eds.), Residue Number System Arithmetic, Modern Applications in Digital Signal Processing, IEEE Press, New York, 1986.
[3]. W.K. Jenkins and B.J. Leon, The use of Residue Number Systems in Design of finite impulse response digital filters, IEEE Trans. Circuits syst. Vol. CSA24, no. 4 pp 191-201. April 1977.
[4]. W. Wang, M.N.S. Swamy, M. Ahmad and Y. Wang, A Comprehensive Study of three Moduli Sets for Residue Arithmetic, Proceedings of the IEEE Canadian Conference on Electrical and Computer Engineering, Edmonton, Alberta, Canada May 9-12 1999.

# International Journal of Engineering Applied Sciences and Technology, 2024 Vol. 8, Issue 11, ISSN No. 2455-2143, Pages 161-173 Published Online March 2024 in IJEAST (http://www.ijeast.com) 

[5]. M.Akkal and P. Siy, A new Mixed Radix Conversion Algorithm MRC-II, Journal of System Architecture 53 (2007) 577-586.
[6]. A.Hiasat and A Sweidan, Residue Number System to Binary Converter for the Moduli set $\left(2^{\mathrm{n}-1}, 2^{\mathrm{n}}-1,2^{\mathrm{n}}+\right.$ 1), Journal of System Architecture 49 (2003) 53-58.
[7]. A.Benjamin Premkumar, An RNS to Binary Converter in $2 \mathrm{n}+1,2 \mathrm{n}, 2 \mathrm{n}-1$ Moduli set, IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, vol. 39, no. 7, July 1992.
[8]. A.Benjamin Premkumar, A RNS to Binary in a three moduli set with common factors, IEEE Transactions on Circuits and systems - II: Analog and Digital Signal Processing, vol. 42, no. 4, April 1995.
[9]. Ahmad A. Hiasat and Hoda S. Abdel-Aty-ZohdyResidue-to-Binary Arithmetic Converter forthe Moduli Set ( $2^{\mathrm{k}}, 2^{\mathrm{k}}-1,2^{\mathrm{k}-1}-1$ ), IEEE Transactions on Circuits and Systems - II: Analog and Digital Signal Processing, vol. 45, no. 2, February 1998.
[10]. S.H. Lin, M.H. Sheu, J.S.Lin, W.T Sheu, Efficient VLSI Design for RNS Reverse Converter Based on New Moduli Set ( $2^{\mathrm{n}}-1,2^{\mathrm{n}}+1,2^{2 \mathrm{n}+1}$ ), IEEE 2006, pp. 2020-2023.
[11]. Yuke Wang, Residue-to-Binary Converters Based on NewChinese Remainder Theorems, IEEE Transactions on Circuits and Systems - II: Analog and Digital Signal Processing, vol. 47, no. 3, March 2000.
[12]. A.SabbaghMolahosseini, M. Kuchaki Rafsanjani, S.H. Ghafouri, M. Hashemipour, A Reduced-Area Reverse Converter for the Moduli Set ( $2^{\mathrm{n}}, 2^{\mathrm{n}}-1,2^{2 \mathrm{n}-1}$ 1), International Journal of Advancements in Computing Technology Volume 2, Number 5, December 2010.
[13]. Arash Hariri, KeivanNavi, Reza Rastegar, A new High Dynamic Range Moduli set with Efficient Reverse Converter, Computers and Mathematics with Applications 55 (2008) 660-668.
[14]. Pemmaraju V. Ananda Mohan,RNS-To-Binary Converter for a New Three-Moduli set $\left\{2^{\text {n+1 }}-1,2^{\text {n }}\right.$, $\left.2^{\text {n }}-1,\right\}$, IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 54, no. 9, September 2007
[15]. M. Bharadwaj, A. B.Premkumar, and T. Srikanthan, Breaking the 2 n -bit carry propagation barrier in residue to binary conversion forthe $\left(2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1\right)$ moduli set, IEEE Trans. Circuits Syst. I,Fundam. Theory Appl., vol. 45, no. 9, pp. 998-1002, Sep. 1998.
[16]. Y.Wang, X. Song, M. Aboulhamid, and H. Shen, Adder-based residue to binary number converters for $\left(2^{n}-1,2^{n}, 2^{n}+1\right)$, IEEE Trans.Signal Processing, vol. 50, no. 7, pp. 1772-1779, Jul. 2002.
[17]. S. J. Piestrak, A high-speed realization of residue to binary system converter, IEEE Trans. Circuits Syst.

II, Analog Digit. Signal Processing, vol. 42, no. 10, pp. 661-663, Oct. 1995.
[18]. A.Dhurkadas, Comments on 'A high-speed realization of a residueto binary number system converter', IEEE Trans. Circuits Syst. II, Analog Digit. Signal Processing, vol. 45, no. 3, pp. 446-447, Mar. 1998
[19]. W. Wang, M.N.S. Swamy,M.O. Ahmad, and Y.Wang, Ahigh-speedresidue-to-binary converter for three-moduli ( $2^{\mathrm{k}}, 2^{\mathrm{k}}-1,2^{\mathrm{k}-1}-1$ )RNS and a scheme for its VLSI implementation, IEEE Trans. CircuitsSyst. II, Analog Digit. Signal Processing, vol. 47, no. 12, pp. 1576-1581, Dec. 2000.
[20]. Tkatchenko, Tatiana V., Yimin Shen, Rod D. Braun, Gurinder Bawa, Pradeep Kumar, Ivan Avrutsky, and Andrei V. Tkatchenko. "Photopic visual input is necessary for emmetropization in mice." Experimental eye research 115 (2013): 87-95.
[21]. Bawa, Gurinder, Tatiana V. Tkatchenko, Ivan Avrutsky, and Andrei V. Tkatchenko. "Variational analysis of the mouse and rat eye optical parameters." Biomedical optics express 4, no. 11 (2013): 25852595.
[22]. Bawa, Gurinder. Modeling of mouse eye and errors in ocular parameters affecting refractive state. Wayne State University, 2013.
[23]. Dajani, O., Bawa, G., \& Singh, H. (2012). VLSI Implementation of Residue Adder and Subtractor. In Proceedings of the International Conference on Frontiers in Education: Computer Science and Computer Engineering (FECS) (p. 1). The Steering Committee of The World Congress in Computer Science, Computer Engineering and Applied Computing (WorldComp).

